

#4

XA-9472

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Yasuhisa SHIMAZAKI et al.

Appln. No.:

Filed: Herewith

For: SEMICONDUCTOR INTEGRATED CIRCUIT

* * *

CHANGE OF CORRESPONDENCE ADDRESS

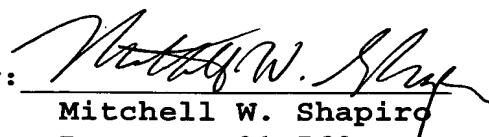
Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Effective immediately, please address all further correspondence in the above-identified application to:

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Respectfully submitted,

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